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Application Number	10/720,297
Filing Date	November 24, 2003
First Named Inventor	Joe A. Ortiz
Art Unit	2838
Examiner Name	Patel, Rajnikant
Attorney Docket Number	PD-03W081

Sheet	1	of	2
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U. S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

Date
Considered

12/16/05

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

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of

2

Application Number

10/120,291

Filing Date

11/24/2003

First Named Inventor

Joe A. Ortiz

Art Unit

2838

Examiner Name

Patel, Rajnikant

Attorney Docket Number

PD-03W-81

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>[Signature]</i>		RAMI, NAIM ET AL: "H Deg Deg Control of Boost Converters: Comparison to Voltage Mode, feedforward, and Current Mode Controls",	
		Record of the Annual Power Electronics Specialists Conference, 12 June 1995, pp 1327-1332, Vol. 2 conf. 26, IEEE, New York, NY, USA	
<i>[Signature]</i>		REDL, R ET AL: "Optimizing the Load Transient Response of the Buck Converter", Applied Power Electronics Conference and Exposition,	
		15 February 1998, pp 170 - 176, Vol. 1, IEEE, New York, NY, USA	
<i>[Signature]</i>		KANEMARU, S ET AL: "Analysis and Optimum Design of a Buck-Type DC-to DC Converter Employing Load current Feedforward", Power Electronics	
		Specialists Conference, 17 May 1998, pp 309-314, Vol. 1, IEEE, New York, NY, USA	

Examiner Signature

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